

Discretional Array Operations for Secure Information Flow

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Abstract. Arrays exist in many nontrivial programs. Array operations can cause subtle information leaks. This paper allows array as first-class value and regards discretional array as array of array by alias array. Arrays are given types of the form τ_1 alias τ_2 , where τ_1 is the security class of the array and τ_2 is the security class of the array's alias. To distinguish array from its alias, we propose a novel binary memory model $[\mu_1; \mu_2]$. The soundness of our type system is proved by noninterference property.

Keywords: alias array, discretional array, secure information flow, type system.

1. Introduction

The static analysis of secure information flow has been studied for many years [1]. The analysis of secure information flow is that checks whether the information flow of a given program is secure. Secure information flow ensures the noninterference, that is, observations of the initial and final values of low-security class (L) program variables do not provide any information about the initial value of high-security class (H) program variables.

For example, in the assignment statement $y := x$, there is an information flow from x to y . If the security class of x is L or H and the security class of y is H , then the information flow x to y is secure. If x is H and y is L , then the information flow x to y is insecure.

In some early work on secure information flow, Denning et al. [2,3,4] proposed a static certification method for verifying the secure flow of information through a program. Program variables are assigned a security class and security classes are assumed to form a lattice structure, ordered by $\leq$. Each variable has a static security class binding that can be determined from the declarations in the program.

Later, Vopano et al. [5,6] developed an elegant syntax-directed type system for annotating program variables, commands, and procedure parameters with security levels. They also proved that their type system ensures noninterference. Geoffrey Smith [7] extend the analysis to deal with a multi-threaded imperative language. The type system is insufficient to ensure noninterference.

Benjamin C. Pierce [8] presents the definition of type system. "A type system is a tractable syntactic method for proving the absence of certain program behaviors by classifying phrases according to the kinds of values they compute." Type system can be used in secrecy analysis.

Banerjee and Naumann [9] extend the type system of Volpano et al. to support a more realistic object oriented sequential language. They proved noninterference for a language with pointers, mutable state, private fields, class-based visibility, dynamic binding and inheritance, type casts, type tests, and mutually recursive classes and methods.

Amer Diwan et al. [10] use programming language types to disambiguate memory reference. They use type compatibility to determine aliases.

Many type systems of secure information flow have been developed, of special mention Deng and Smith's type system focused on one-dimensional integer array operations for practical secure information

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flow [11]. Arrays are given types of the form $\tau_1 \text{ arr } \tau_2$, where τ_1 is the security class of the array's contents and τ_2 is the security class of the array's length. The type system ensures noninterference property. In their system, a multidimensional-array isn't regarded as array of array, for they do not treat arrays as first-class values for simplicity.

Arrays play a major role in many nontrivial programs, but array operations can cause information leaks. For example, the program

```

i = 0;
int a0[5] = {0,1,2,3,4};
int a1[5] = {5,6,7,8,9};
int a[2];
while (i < 2){
    &xi = ai[5];
    a[i] = xi;
    i = i + 1;
}

```

Here, x denotes an alias of the one-dimensional array $a_i[5]$. There are some information flows [4] from $a_i[5]$ to the alias x_i . Hence if $a_i[5]$ is H , then we must make the alias x_i be H as well.

In our system, arrays are treated as first-class values and a discretional array may be regarded as array of array. arrays are given types of the form $\tau_1 \text{ alias } \tau_2$; where τ_1 is the security class of the array and τ_2 is the security class of its alias. Several combinations are useful: $L \text{ alias } L$ is an array whose both contents and its alias are private, $H \text{ alias } H$ is an array whose both contents and its alias are public.

The remainder of the paper is organized as follows. In Section 2, we describe the simple sequential imperative language that we consider, and formally define its semantics for array operations. In Section 3 we present the details of our type system, and in Section 4 we prove that it guarantees a noninterference property. Finally, Section 5 gives our conclusions.

2. Syntax and Semantics

Our typed language is the simple imperative language[4] with arrays. The syntax of the language is as follows:

(phrases) $p ::= e \mid c$

(expressions) $e ::= x \mid n \mid x[e] \mid \&x \mid e_1 \div e_2 \mid e_1 * e_2 \mid e_1 \vee e_2 \mid e_1 \wedge e_2 \mid e_1 + e_2 \mid e_1 - e_2 \mid e_1 = e_2 \mid e_1 \neq e_2 \mid e_1 < e_2 \dots$

(commands) $c ::= x := e \mid x[e_1] := e_2 \mid \text{allocate } x[e] \mid \&x := x[e] \mid x[e_1] := \&x \mid \text{skip} \mid$
 $\text{if } e \text{ then } c_1 \text{ do } c_2 \mid \text{while } e \text{ do } c \mid c_1; c_2$

Here, meta-variable x ranges over identifiers and n over integer literals. 0 stands for false and 1 for true. The expression $x[e]$ is one-dimensional array. The expression $\&x$ is an alias of an array. The command $\text{allocate } x[e]$ allocates a 0-initialized block of memory for one-dimensional array $x[e]$; the size of the array is given by e . The command $\&x := x[e]$ declares that $\&x$ is an alias of the array $x[e]$. The command $x[e_1] := \&x$ initializes the elements of an array by alias.

A program c is executed under a memory, which maps identifiers to values. A value is either an integer

n a multidimensional-array $[x_0, x_1, x_2, \dots, x_{k-1}]$, where $k \geq 0$, $x_0, x_1, x_2, \dots, x_{k-1}$ are aliases of inferior-dimensional-array. An array and its alias have same address, so two identifiers a and b can point to the same block of memory. A binary memory $[\mu_1; \mu_2]$ is needed for distinguished them. A program c is executed under the binary memory $[\mu_1; \mu_2]$. μ_1 maps identifiers to values. A value is either an integer n or a multidimensional-array $[x_0, x_1, x_2, \dots, x_{k-1}]$, where $k \geq 0$, $x_0, x_1, x_2, \dots, x_{k-1}$ are aliases of inferior-dimensional-array. μ_2 maps aliases identifiers to values.

The formal semantics of commands and expressions is given by a standard structural operational semantics. See Figure 1.

$$\begin{array}{l}
 \text{(UPDATE)} \quad \frac{x \in \text{dom}(\mu_1)}{(x := e, [\mu_1; \mu_2]) \Rightarrow [\mu_1[x := \mu_1(e)]; \mu_2]} \\
 \text{(NO-OP)} \quad (skip, [\mu_1; \mu_2]) \Rightarrow [\mu_1; \mu_2] \\
 \text{(BRANCH)} \quad \frac{\mu_1(e) = 1}{(\text{if } e \text{ then } c_1 \text{ else } c_2, [\mu_1; \mu_2]) \Rightarrow (c_1, [\mu_1; \mu_2])} \\
 \quad \frac{\mu_1(e) = 0}{(\text{if } e \text{ then } c_1 \text{ else } c_2, [\mu_1; \mu_2]) \Rightarrow (c_2, [\mu_1; \mu_2])} \\
 \text{(LOOP)} \quad \frac{\mu_1(e) = 0}{(\text{while } e \text{ do } c, [\mu_1; \mu_2]) \Rightarrow [\mu_1; \mu_2]} \\
 \quad \frac{\mu_1(e) = 1}{(\text{while } e \text{ do } c, [\mu_1; \mu_2]) \Rightarrow (c; \text{while } e \text{ do } c, [\mu_1; \mu_2])} \\
 \text{(SEQUENCE)} \quad \frac{(c_1, [\mu_1; \mu_2]) \Rightarrow [\mu'_1; \mu_2]}{(c_1; c_2, [\mu_1; \mu_2]) \Rightarrow (c_2, [\mu'_1; \mu_2])} \\
 \quad \frac{(c_1, [\mu_1; \mu_2]) \Rightarrow (c'_1, [\mu'_1; \mu_2])}{(c_1; c_2, [\mu_1; \mu_2]) \Rightarrow (c'_1; c_2, [\mu'_1; \mu_2])}
 \end{array}$$

Figure1. The formal semantics of commands and expressions

The formal semantics of array commands and expressions are given in Figure 2. We prescribe an array indexes with an out-of-bounds indices yields 0 and an array initializes with an out-of-bounds indices is skipped.

$$\begin{array}{l}
 \text{(ARR-INDEX)} \quad \frac{x \in \text{dom}(\mu_1), [\mu_1; \mu_2](x) = [[x_0, \dots, x_{k-1}]; \mu_2], \mu_1(e) = i, 0 \leq i < k}{[\mu_1; \mu_2](x[e]) = [x_i; \mu_2]} \\
 \quad \frac{x \in \text{dom}(\mu_1), [\mu_1; \mu_2](x) = [[x_0, \dots, x_{k-1}]; \mu_2], \mu_1(e) = i, i < 0 \vee i \geq k}{\begin{bmatrix} \mu_1 \\ \mu_2 \end{bmatrix}(x[e]) = [0; \mu_2]}
 \end{array}$$

$$\begin{array}{l}
\text{(DIV)} \quad \frac{\mu(e_1) = n, \mu(e_2) = 0}{\mu(e_1 \div e_2) = 0} \\
\frac{\mu(e_1) = n_1, \mu(e_2) = n_2, n \neq 0}{\mu(e_1 \div e_2) = \lfloor n_1 \div n_2 \rfloor} \\
\text{(UPDATE-ARR)} \quad \frac{x \in \text{dom}(\mu_1), [\mu_1; \mu_2](x) = \llbracket [x_0, \dots, x_{k-1}]; \mu_2 \rrbracket, \mu_1(e) = i, 0 \leq i < k}{(x[e] = e_1, [\mu_1; \mu_2]) \Rightarrow [\mu_1[x_i := \mu_1(e_1)]; \mu_2]} \\
\frac{x \in \text{dom}(\mu_1), [\mu_1; \mu_2](x) = \llbracket [x_0, \dots, x_{k-1}]; \mu_2 \rrbracket, \mu_1(e) = i, i < 0 \vee i \geq k}{(x[e] = e_1, [\mu_1; \mu_2]) \Rightarrow [\mu_1; \mu_2]} \\
\text{(CALLOC)} \quad \frac{x \in \text{dom}(\mu_1), \mu_1(e) \geq 0}{(\text{allocat } x[e], [\mu_1; \mu_2]) \Rightarrow \left[\mu_1 \left\langle x := \underbrace{[0, 0, \dots, 0]}_{\mu_1(e) \text{ of these}} \right\rangle; \mu_2 \right]} \\
\frac{x \in \text{dom}(\mu_1), \mu_1(e) < 0}{(\text{allocat } x[e], [\mu_1; \mu_2]) \Rightarrow [\mu_1 \langle x := 0 \rangle; \mu_2]} \\
\text{(CALIAS)} \quad \frac{x \in \text{dom}(\mu_1), [\mu_1; \mu_2](x) = \llbracket [x_0, \dots, x_{k-1}]; \mu_2 \rrbracket}{(\&x := x[e], [\mu_1; \mu_2]) \Rightarrow \llbracket [x_0, \dots, x_{k-1}]; [x_0, \dots, x_{k-1}] \rrbracket} \\
x \in \text{dom}(\mu_1), \&x \in \text{dom}(\mu_1), [\mu_1; \mu_2](x) = \llbracket [x_0, \dots, x_{k-1}]; \mu_2 \rrbracket, \\
[\mu_1; \mu_2](\&x) = [\mu_1; [x_0, \dots, x_{k-1}]], \\
\mu_1(e) = i, 0 \leq i \leq k \\
\frac{(x[e] := \&x, [\mu_1; \mu_2]) \Rightarrow [\mu_1 \langle x_i := x \rangle; [x_0, \dots, x_{k-1}]]}{x \in \text{dom}(\mu_1), \&x \in \text{dom}(\mu_2), [\mu_1; \mu_2](x) = \llbracket [x_0, \dots, x_{k-1}]; \mu_2 \rrbracket, \\
[\mu_1; \mu_2](\&x) = [\mu_1; [x_0, \dots, x_{k-1}]], \mu_1(e) = i, 0 \leq i \leq k} \\
(x[e] := \&x, [\mu_1; \mu_2]) \Rightarrow [\mu_1 \langle x_i := x \rangle; [x_0, \dots, x_{k-1}]]
\end{array}$$

Figure2. The formal semantics of array commands and expressions

3. The Type System

In this section, we extend the typing rules in [5] with new rules for typing array operations. We type array using a type of the form $\tau_1 \text{ alias } \tau_2$; where τ_1 is the security class of the array and τ_2 is the security class of its alias.

Here are the types used by our system:

$$\begin{array}{l}
\text{(data types)} \quad \tau ::= L \mid H \\
\text{(phrase types)} \quad \rho ::= \tau \mid \tau_{\text{var}} \mid \tau_{\text{cmd}} \mid \tau_1 \text{ alias } \tau_2
\end{array}$$

For simplicity, we limit the security classes here to just L and H ; it is possible to generalize to an arbitrary lattice of security classes. Type τ_{var} is the type of a variable and τ_{cmd} is the type of a command.

All of the four possible array types ($H \text{ alias } H$, $H \text{ alias } L$, $L \text{ alias } H$, $L \text{ alias } L$), $L \text{ alias } H$ do not really make sense. Although an L -array can be aliased by L -variable and H -variable, a type L -array never is aliased an H -variable[9]. So if an array is of type L , then its alias must be L . $H \text{ alias } L$ do not really make sense too. There is an information flow [4] from an array to its alias. Hence if an array is of type H , then its alias must be H . We therefore adopt the following constraint globally:

Global Array Constraint: In any array type τ_1 *alias* τ_2 , we require that $\tau_2 = \tau_1$.

We can now present our type system formally. It allows us to prove typing judgments of the form $\gamma|-p:\rho$ as well as subtyping judgments of the form $\rho \subseteq \rho'$. Here γ denotes an identifier typing, which maps identifiers to phrase types of the form τ_{var} or τ_1 *alias* τ_2 . The typing judgment $\gamma|-p:\rho$ means that phrase p has type ρ , assuming γ prescribes types for any free identifiers in p . The subtype relation is antimonotonic in the types of commands, meaning that if $\tau \subseteq \tau'$ then $\tau'_{\text{cmd}} \subseteq \tau_{\text{cmd}}$. As usual, there is a type coercion rule that allows a phrase of type ρ to be assigned a type ρ' whenever $\rho \subseteq \rho'$.

The typing rules are given in Figure 3 and the subtyping rules in Figure 4.

(R-VAL)	$\frac{\gamma(x) = \tau \text{ var}}{\gamma -x:\tau}$
(ARRAY)	$\frac{\gamma(x) = \tau_1 \text{ alias } \tau_2}{\gamma -x[e]:\tau_1}$
(INT)	$\gamma -n:L$
(QUOTIENT)	$\frac{\gamma -e_1:\tau, \gamma -e_2:\tau}{\gamma -e_1 \div e_2:\tau}$
(ADDITION)	$\frac{\gamma -e_1:\tau, \gamma -e_2:\tau}{\gamma -e_1 + e_2:\tau}$
(ASSIGN)	$\frac{\gamma(x) = \tau_{\text{var}}, \gamma -e:\tau}{\gamma -x := e:\tau_{\text{cmd}}}$
(ASSIGN-ARR)	$\frac{\gamma(x) = \tau_1 \text{ alias } \tau_2, \gamma -e_1:\tau_1, \gamma -e_2:\tau_1}{\gamma -x[e_1] := e_2:\tau_{1\text{cmd}}}$
(ALLOCATE)	$\frac{\gamma(x) = \tau_1 \text{ alias } \tau_2, \gamma -e:\tau_1}{\gamma -allocate\ x[e]:\tau_{1\text{cmd}}}$
(ALIAS)	$\frac{\gamma(x) = \tau_1 \text{ alias } \tau_2}{\gamma - \& x:\tau_2}$ $\frac{\gamma(x) = \tau_1 \text{ alias } \tau_2}{\gamma - \& x := x[e]:\tau_{2\text{cmd}}}$ $\frac{\gamma(x) = \tau_1 \text{ alias } \tau_2}{\gamma -x[e_1] := \& x:\tau_{1\text{cmd}}}$
(SKIP)	$\gamma -skip:H_{\text{cmd}}$
(IF)	$\gamma -e:\tau, \gamma -c_1:\tau_{\text{cmd}},$ $\gamma -c_2:\tau_{\text{cmd}}$ $\frac{}{\gamma -if\ e\ then\ c_1\ else\ c_2:\tau_{\text{cmd}}}$
(WHILE)	$\frac{\gamma -e:\tau, \gamma -c:\tau_{\text{cmd}}}{\gamma -while\ e\ do\ c:\tau_{\text{cmd}}}$

$$\begin{array}{c}
 \gamma \mid -c_1 : \tau_{cmd} , \\
 \gamma \mid -c_2 : \tau_{cmd} \\
 \hline
 \gamma \mid -c_1 ; c_2 : \tau_{cmd}
 \end{array}$$

(COMPOSE)

Figure 3. Typing Rules

$$\begin{array}{c}
 L \subseteq H \\
 \hline
 \tau' \subseteq \tau \\
 \tau_{cmd} \subseteq \tau'_{cmd} \\
 \rho \subseteq \rho \\
 \hline
 \rho_1 \subseteq \rho_2, \rho_2 \subseteq \rho_3 \\
 \hline
 \rho_1 \subseteq \rho_3 \\
 \hline
 \gamma \mid -p : \rho_1, \rho_1 \subseteq \rho_2 \\
 \hline
 \gamma \mid -p : \rho_2
 \end{array}$$

(BASE)

(CMD-)

(REFLEX)

(TRANS)

(SUBSUMP)

Figure 4. Subtyping Rules

Now, we discuss the array typing rules. In rule ARRAY, the security class of expression $x[e]$ depends on the contents of the array x as well as on the alias of the array. Given the Global Array Constraint, this simplifies to τ_1 .

For rule ASSIGN-ARR, if $x : H \text{ alias } L$, then the command $x[e_1] := e_2$ can be given type H_{cmd} , which intuitively says that it only assigns to H variables. If $x : L \text{ alias } L$, then the command $x[e_1] := e_2$ can be given type L_{cmd} , which intuitively says that it only assigns to L variables. These are valid because they do change the alias of the array $x[e]$.

For rule ALLOCATE, the command `allocate  $x[e]$` do assign a length of index to the array x , and every element of the array $x[e]$ can later be initialized by alias $\&x$.

For rule ALIAS, $\&x$ is an alias of x ; If $x : \tau_1 \text{ alias } \tau_2$, then $\&x := x[e] : \tau_{2cmd}$. If $x : \tau_1 \text{ alias } \tau_2$, then $x[e_1] := \&x : \tau_{1cmd}$.

4. Type Soundness as Noninterference

In this section, we establish the semantics soundness of our type system by proving a noninterference theorem.

Definition 4.1: Given an identifier typing γ , if $[\mu_1; \mu_2], [\nu_1; \nu_2]$ and γ have the same domain and $[\mu_1; \mu_2]$ and $[\nu_1; \nu_2]$ agree on all L identifiers, then memories $[\mu_1; \mu_2]$ and $[\nu_1; \nu_2]$ are equivalent. This is written $[\mu_1; \mu_2] \sim_\gamma [\nu_1; \nu_2]$.

Lemma 4.1 (Simple Security): If $\gamma \mid -e : L$ and $[\mu_1; \mu_2] \sim_\gamma [\nu_1; \nu_2]$, then $[\mu_1; \mu_2](e) = [\nu_1; \nu_2](e)$.

Proof. By induction on the structure of e :

1. The case x . By typing rule R-VAL and $\gamma \mid -x : L$, $\gamma(x) = L_{var}$. We have $[\mu_1; \mu_2](x) = [\nu_1; \nu_2](x)$.
2. The case $x[e]$. By typing rule ARRAY and $\gamma \mid -x[e] : L$, we have $\gamma(x) = L \text{ alias } L$, $\gamma \mid -e : L$, for some τ . By the definition of memory equivalence, we have $[\mu_1; \mu_2](x[e]) = [\nu_1; \nu_2](x[e])$.

3. The case $\&x$. By typing rule ALIAS and $\gamma \vdash \&x : L$, we have $\gamma(x) = \tau \text{ alias } L$, for some τ . By memory equivalence, we have $[\mu_1; \mu_2](\&x) = [\nu_1; \nu_2](\&x)$.

4. The case $e_1 \div e_2$. By typing rule QUOTIENT and $\gamma \vdash e_1 : L$, $\gamma \vdash e_2 : L$, we have $\gamma \vdash e_1 \div e_2 : L$, $[\mu_1; \mu_2](e_1) = [\nu_1; \nu_2](e_1)$ and $[\mu_1; \mu_2](e_2) = [\nu_1; \nu_2](e_2)$, then

$$[\mu_1; \mu_2](e_1 \div e_2) = [\nu_1; \nu_2](e_1 \div e_2).$$

5. The case $e_1 + e_2$. By typing rule ADDITION and $\gamma \vdash e_1 : L$, $\gamma \vdash e_2 : L$, we have $\gamma \vdash e_1 + e_2 : L$, $[\mu_1; \mu_2](e_1) = [\nu_1; \nu_2](e_1)$ and $[\mu_1; \mu_2](e_2) = [\nu_1; \nu_2](e_2)$, then

$$[\mu_1; \mu_2](e_1 + e_2) = [\nu_1; \nu_2](e_1 + e_2)..$$

6. The other cases $e_1 \times e_2, e_1 \vee e_2, e_1 \wedge e_2, e_1 = e_2, e_1 \neq e_2, e_1 < e_2$.

Each one of them is similar to The case $e_1 \div e_2$ and The case $e_1 + e_2$.

Lemma 4.2 (Confinement): If $\gamma \vdash c : H_{cmd}$ and $(c, [\mu_1; \mu_2]) \rightarrow (c', [\mu'_1; \mu'_2])$ or $(c, [\mu_1; \mu_2]) \rightarrow [\mu'_1; \mu'_2]$, then $[\mu_1; \mu_2] \sim_\gamma [\mu'_1; \mu'_2]$.

Proof. This proof is by induction on the structure of the commands c :

1. The case $x := e$. Here we have $\gamma(x) = H_{var}$, so $[\mu_1; \mu_2] \sim_\gamma [\mu_1[x := \mu(e)]; \mu_2]$.
2. The case $x[e_1] := e_2$. Here we have $\gamma(x) = H \text{ alias } H$. Hence by rule UPDATE-ARR we have $[\mu_1; \mu_2] \sim_\gamma [\mu'_1; \mu'_2]$.
3. The case *allocate* $x[e]$.

Here we have $\gamma(x) = H \text{ alias } H$, so by rule CALLOCATE, we have $[\mu_1; \mu_2] \sim_\gamma [\mu'_1; \mu'_2]$.

4. The case $\&x := x[e]$. Here we have $\gamma(x) = H \text{ alias } H$. Hence by rule CALIAS, we have $[\mu_1; \mu_2] \sim_\gamma [\mu'_1; \mu'_2]$.
5. The case $x[e_1] := \&x$. Here we have $\gamma(x) = H \text{ alias } H$. Hence by rule CALIAS, we have $[\mu_1; \mu_2] \sim_\gamma [\mu'_1; \mu'_2]$.
6. The case *skip*, if e then c_1 else c_2 , and while e do c . These cases are trivial, because $[\mu_1; \mu_2] = [\mu'_1; \mu'_2]$.
7. The case $c_1; c_2$.

The command $c_1; c_2$ is the composition of two commands c_1 and c_2 . The lemma follows directly by induction.

Lemma 4.3 (Subject Reduction): If $\gamma \vdash c : \tau_{cmd}$ and $(c, [\mu_1; \mu_2]) \rightarrow (c', [\mu'_1; \mu'_2])$, then $\gamma \vdash c : \tau'_{cmd}$.

Proof. This proof is by induction on the structure of the commands c :

1. The case $c_1; c_2$.

If c is of the form $c_1; c_2$, then it follows that $\gamma \vdash c_1 : \tau_{cmd}$ and $\gamma \vdash c_2 : \tau_{cmd}$. By rule SEQUENCE, c' is either c_2 or else $c'_1; c_2$, where $(c_1, [\mu_1; \mu_2]) \rightarrow (c'_1, [\mu'_1; \mu'_2])$. For the first case, we have $\gamma \vdash c_2 : \tau_{cmd}$. For

the second case, we have $\gamma|-c_1 : \tau_{cmd}$ hence by induction we have $\gamma|-c'_1; c_2 : \tau_{cmd}$ by rule COMPOSE.

2. The case while e do c

If c is the form of while e do c , then τ must be L , we must have $\gamma|-c : L_{cmd}$, and so $\gamma|-c; \text{while } e \text{ do } c : L_{cmd}$.

3. The case of if e then c_1 else c_2 .

The case of if e then c_1 else c_2 is similar to the case while e do c .

Lemma 4.4: If $(c_1; c_2, [\mu_1; \mu_2]) \rightarrow^k [\mu'_1; \mu'_2]$, then there exist $0 < j < k$ and $[\mu''_1; \mu''_2]$ such that $(c_1, [\mu_1; \mu_2]) \rightarrow^j [\mu''_1; \mu''_2]$, and $(c_2, [\mu''_1; \mu''_2]) \rightarrow^{k-j} [\mu'_1; \mu'_2]$. $\rightarrow^k$ denotes k -fold self composition of $\rightarrow$.

Proof. By induction on k . If the derivation begins with an application of the first SEQUENCE rule, then there exists $[\mu''_1; \mu''_2]$ such that

$$(c_1, [\mu_1; \mu_2]) \rightarrow [\mu''_1; \mu''_2]$$

and

$$(c_1; c_2, [\mu_1; \mu_2]) \rightarrow [\mu''_1; \mu''_2] \rightarrow^{k-1} [\mu'_1; \mu'_2].$$

So we can let $j = 1$. And, since $k - 1 \geq 1$, we have $j < k$.

If the derivation begins with an application of the second SEQUENCE rule, then there exists c'_1 and $[\mu_{11}; \mu_{21}]$ such that

$$(c_1, [\mu_1; \mu_2]) \rightarrow (c'_1, [\mu_{11}; \mu_{21}])$$

and

$$(c_1; c_2, [\mu_1; \mu_2]) \rightarrow (c'_1; c_2, [\mu_{11}; \mu_{21}]) \rightarrow^{k-1} [\mu'_1; \mu'_2].$$

By induction, there exists j and $[\mu''_1; \mu''_2]$ such that

$$0 < j < k - 1, (c'_1, [\mu_{11}; \mu_{21}]) \rightarrow^j [\mu''_1; \mu''_2],$$

and

$$(c_2, [\mu''_1; \mu''_2]) \rightarrow^{k-1-j} [\mu'_1; \mu'_2].$$

Hence

$$(c_1, [\mu_1; \mu_2]) \rightarrow^{j+1} [\mu''_1; \mu''_2]$$

and

$$(c_2, [\mu''_1; \mu''_2]) \rightarrow^{k-(j+1)} [\mu'_1; \mu'_2].$$

And $0 < j + 1 < k$.

Theorem 4.6 (Noninterference): Suppose that

$$\gamma|-c : \tau_{cmd}, \text{ and } [\mu_1; \mu_2] \sim_\gamma [\nu_1; \nu_2].$$

If

$$(c, [\mu_1; \mu_2]) \rightarrow^* [\mu'_1; \mu'_2] \text{ and } (c, [\nu_1; \nu_2]) \rightarrow^* [\nu'_1; \nu'_2],$$

then

$$[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2] .$$

Proof. By induction on the length of the execution $(c, [\mu_1; \mu_2]) \rightarrow^* [\mu'_1; \mu'_2]$. We consider the different forms of c :

1. The case $x := e$

By rule UPDATE, we have $[\mu'_1; \mu'_2] = [\mu_1[x := \mu(e)]; \mu_2]$ and $[\nu'_1; \nu'_2] = [\nu_1[x := \nu(e)]; \nu_2]$. If $\gamma(x) = L_{\text{var}}$, then by rule ASSIGN, we have $\gamma \vdash e : L$. So by Simple Security, we have

$$[\mu_1; \mu_2](e) = [\nu_1; \nu_2](e) ,$$

So

$$[\mu_1; \mu_2][x := \mu(e)] \sim_\gamma [\nu_1; \nu_2][x := \nu(e)] .$$

If $\gamma(x) = H_{\text{var}}$, then trivially $[\mu_1; \mu_2][x := \mu(e)] \sim_\gamma [\nu_1; \nu_2][x := \nu(e)]$.

2. The case *skip*.

The result follows immediately from rule NO-OP.

3. The case $c_1; c_2$.

If $(c_1; c_2, [\mu_1; \mu_2]) \rightarrow^k [\mu'_1; \mu'_2]$ then by Lemma 4.4 there exist $0 < j < k$ and $[\mu''_1; \mu''_2]$ such that,

$$(c_1, [\mu_1; \mu_2]) \rightarrow^j [\mu''_1; \mu''_2] ,$$

and

$$(c_2, [\mu''_1; \mu''_2]) \rightarrow^{k-j} [\mu'_1; \mu'_2] .$$

Similarly, If $(c_1; c_2, [\nu_1; \nu_2]) \rightarrow^{k'} [\nu'_1; \nu'_2]$, then there exist $0 < j' < k'$ and $[\nu''_1; \nu''_2]$, such that

$$(c_1, [\nu_1; \nu_2]) \rightarrow^{j'} [\nu''_1; \nu''_2] ,$$

and

$$(c_2, [\nu''_1; \nu''_2]) \rightarrow^{k'-j'} [\nu'_1; \nu'_2] .$$

By induction, we have $[\mu''_1; \mu''_2] \sim_\gamma [\nu''_1; \nu''_2]$. So by induction again, we have $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

4. The case if e then c_1 else c_2 .

If $\gamma \vdash e : L$ then $\mu_1(e) = \nu_1(e)$ by Simple Security. If $\mu_1(e) \neq 0$ then have the form

$$(\text{if } e \text{ then } c_1 \text{ else } c_2, [\mu_1; \mu_2]) \rightarrow (c_1, [\mu_1; \mu_2]) \rightarrow^* [\mu'_1; \mu'_2]$$

and

$$(\text{if } e \text{ then } c_1 \text{ else } c_2, [\nu_1; \nu_2]) \rightarrow (c_1, [\nu_1; \nu_2]) \rightarrow^* [\nu'_1; \nu'_2]$$

By induction, we have $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$. The case when $\mu_1(e) = 0$ is similar.

If $\gamma \not\vdash e : L$, we have $\gamma \vdash \text{if } e \text{ then } c_1 \text{ else } c_2 : H_{\text{cmd}}$. Then by Confinement, we have

$$[\mu_1; \mu_2] \sim_\gamma [\mu'_1; \mu'_2] \text{ and } [\nu_1; \nu_2] \sim_\gamma [\nu'_1; \nu'_2] .$$

So $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

5. The case *while* e *do* c .

Similar to the case if e then c_1 else c_2 .

6. The case $\text{allocate } x[e]$.

We consider the possible types of x . If $\gamma(x) = L \text{ alias } L$. By Simple Security, we have $[\mu_1; \mu_2](x[e]) = [\nu_1; \nu_2](x[e])$. So $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

If $\gamma(x) = H \text{ alias } H$, by rule ALLOCATE we have $\gamma \mid \text{allocate } x[e] : H_{cmd}$. By the Confinement, we have $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

7. The case $x[e_1] := e_2$.

We consider the possible types of x .

If $\gamma(x) = L \text{ alias } L$, then by rule ASSIGN-ARR we have $\gamma \mid x[e_1] : L$, $\gamma \mid e_2 : L$. By Simple Security, we have $[\mu_1; \mu_2](x[e_1]) = [\nu_1; \nu_2](x[e_1])$, $[\mu_1; \mu_2](e_2) = [\nu_1; \nu_2](e_2)$.

So by rule UPDATE-ARR, we have $[\mu'_1; \mu'_2](x) = [\nu'_1; \nu'_2](x)$, So, $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

If $\gamma(x) = H \text{ alias } H$, then by rule ASSIGN-ARR we have $\gamma \mid x[e_1] := e_2 : H_{cmd}$. By Confinement, we have $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

8. The case $\&x := x[e]$.

We consider the possible types of x .

If $\gamma(x) = H \text{ alias } H$, then by rule CALIAS we have $\gamma \mid \&x := x[e] : H_{cmd}$. By Confinement, we have $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

If $\gamma(x) = L \text{ alias } L$, then by rule ALIAS we have $\gamma \mid \&x : L$, $\gamma \mid x[e] : L$. By Simple Security, we have $[\mu_1; \mu_2](x[e]) = [\nu_1; \nu_2](x[e])$, $[\mu_1; \mu_2](\&x) = [\nu_1; \nu_2](\&x)$. So by rule CALIAS, we have $[\mu'_1; \mu'_2](\&x) = [\nu'_1; \nu'_2](\&x)$, So, $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

9. The case $x[e_1] := \&x$.

We consider the possible types of x .

If $\gamma(x) = H \text{ alias } H$, then by rule CALIAS we have $\gamma \mid x[e_1] := \&x : H_{cmd}$. By Confinement, we have $[\mu_1; \mu_2] \sim_\gamma [\mu'_1; \mu'_2]$.

If $\gamma(x) = L \text{ alias } L$, then by rule ALIAS we have $\gamma \mid \&x : L$, $\gamma \mid x[e_1] : L$. By Simple Security, we have $[\mu_1; \mu_2](x[e_1]) = [\nu_1; \nu_2](x[e_1])$, $[\mu_1; \mu_2](\&x) = [\nu_1; \nu_2](\&x)$. So by rule CALIAS, we have $[\mu'_1; \mu'_2](x) = [\nu'_1; \nu'_2](x)$, So, $[\mu'_1; \mu'_2] \sim_\gamma [\nu'_1; \nu'_2]$.

5. Conclusion

This paper allows arrays as first-class value, we can deal with array of arbitrary values. Our array types are of the form $\tau_1 \text{ alias } \tau_2$, This allow us to regard freely discretional array as array of array by alias array. The soundness of our type system is proved by noninterference.

For the example program in Section 1. If array a_i have types $H \text{ alias } H$ and $L \text{ alias } L$, the information flow from the a_i to alias x_i are secure.

In the future, we expect new type systems to support for secure information flow in more expressive languages. More generally, we expect to make secure information flow type system practical. The works of Stephen Chong, A.C.Myers [15] and Peng Li, Steve Zdancewic [16] are promising.

6. References

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