

Component-Level Reduction Rules for Time Petri Nets Based on DTPN

Jinlong Jiang¹⁺, Xianzhong Zhou² and Yongcheng Sun¹

¹Department of Automation, Nanjing University of Science and Technology, Nanjing 210094, China

²School of Management and Engineering, Nanjing university, Nanjing 210093, China

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Abstract. Time Petri Nets (TPNs) are a popular Petri net model for specification and verification of real-time systems. A widely applied method for analyzing Petri nets is component-level reduction analysis. The existing technique for component-level reduction analysis transforms a TPN component to a constant size of simple one while maintains the net external observable timing properties, but it neglects the internal properties of component such as synchronization, conflict and concurrency. Based on Delay Time Petri Net (DTPN), the paper transforms a TPN component to DTPN model in order to preserve such properties as synchronization, conflict and concurrency during the reduction. For the sake of analyzing the DTPN model, the paper proposes new schedule analysis method. Finally, reduction rules based on DTPN are applied to the TPN model analysis in the command and control (C2) system.

Keywords: component-level reduction rules; DTPN model; new DTPN' schedule analysis method; C2 system.

1. Introduction

TPNs are a most widely used model for real-time system specification and verification [1,2,3,4]. A fundamental and most widely applied method for analyzing Petri net's model is reachability analysis. It can enumerate all the reachable state then gets the state transition graph [2]. This representation makes explicit such properties as deadlock freedom and reachability. For a complex or even middle-sized TPN, however, it is difficult to enumerate its reachable state, which is commonly referred to state-explosion problem. Sloan et al. developed reduction rules about place fusion and transition fusion, which works at individual place and transition level [5]. But these reduction rules contain such defects as inefficiency of verification and frequency of operation. It is necessary to reduce TPN model at a coarse grained level so as to make TPN model expedite constringency.

A set of component-level reduction rules for TPN model is proposed in [4]. Each of reduction rules transforms a TPN component to a constant size of simple one while maintains the net's external observable timing properties. It works at a much coarser level than those developed by Sloan et al., and fewer applications of these rules are needed to reduce the size of the TPN. These rules reduce the complexity of TPN, however, neglect the internal properties of component such as conflict, synchronization and concurrency. It leads to the occurrence of events that shouldn't be taken place. In figure 1 (a), t_4 should happen before t_6 , so place p_9 cannot get one token. But p_7 and p_9 have the same chance to get one token in figure 1 (b). It is obvious that TPN model after transformation is not consistent with TPN model before transformation. This paper introduces DTPN [6] and transformations a TPN component to DTPN. It preserves the external observable timing properties while maintains such properties as conflict, synchronization and concurrency.

A state of DTPN is a pair $S = (M, \Theta)$ [6], where:

- $M = (p_i[EDP_i, LDP_i], \dots)$;

⁺ Corresponding author.

E-mail address: jjlong88@126.com

- Θ is a global (dynamic) time. It indicates that one token arrives at P_i in the dynamic interval $[EDP_i + \theta, LDP_i + \theta]$.

At run time, a set of dynamic intervals EA is associated with each state S. $EA = (TOK_{ij}(p_j, t_i)[\theta E_{ai}, LE_{ai}], \dots)$, which indicated that token TOK_{ij} in place p_j is able to enable transition t_i after a time in the dynamic interval $[\theta E_{ai}, \theta L_{ai}]$. A set of dynamic transition firing intervals FT is associated with each state S. $FT = (t_i[\theta ET_i, \theta LT_i] \{TOK_{i1}, \dots, TOK_{in}\}, \dots)$, which indicated that transition t_i must fire in the dynamic interval $[\theta ET_i, \theta LT_i]$ by using the token set $\{TOK_{i1}, \dots, TOK_{in}\}$ if no token in this token set is removed before the firing.

Assume the current state is S. When a transition t_i in FT fires at dynamic time θ_1 , a new state S' is obtained by:

- ① Changing the dynamic time θ into θ_1 .
- ② Removing $p_j[EDP_j, LDP_j]$ from M , where p_j is an input place of transition t_i . Adding $p_k[EDP_k, LDP_k]$ into M , where p_k is an output place of transition t_i . Modifying $p_n[EDP_n, LDP_n]$, where p_n is corresponding to a token not used for transition t_i firing, $EDP_n' = \max(0, EDP_n + \theta - \theta_1)$, $LDP_n' = \max(LDP_n + \theta - \theta_1)$.
- ③ Updating EA. Removing $TOK_{mj}(p_j, t_m)[\theta E_{Am}, \theta L_{Am}]$, where t_m is the output transition of place p_j . Adding $TOK_{sk}(p_k, t_s)[\theta E_{As}, \theta L_{As}]$, where t_s is the output transition of place p_k . $[\theta E_{As}, \theta L_{As}] = [\theta_1, \theta_1] + [ED_{Aik}, LD_{Aik}] + [ED_{Aks}, ED_{Aks}]$, where $[ED_{Aik}, LD_{Aik}]$ is the static interval on $arc(t_i, p_k)$, $[ED_{Aks}, LD_{Aks}]$ is the static interval on $arc(p_k, t_s)$.
- ④ Updating FT. $t_s[\theta ET_s, \theta LT_s] \{TOK_{s1}, \dots, TOK_{sl}, \dots, TOK_{sn}\}$ is added into FT if there exists an element $TOK_{sl}(p_l, t_s) \in EA_{k+1}, l = 1, \dots, n$ in EA for each input place p_l of transition t_s , where $[\theta ET_s, \theta LT_s] = [ET_s, LT_s] + MAX_{p_l}([\theta E_{Als}, \theta L_{Als}])$, $[ET_s, LT_s]$ is the static interval of t_s , $[\theta E_{Als}, \theta L_{Als}]$ is the dynamic interval for $TOK_{sl}(p_l, t_s)$ in the updated EA.

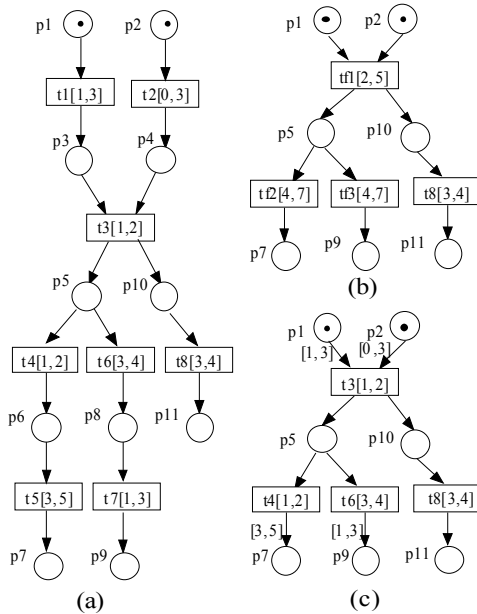


Figure 1 (a) TPN model (b) TPN model after reduction (c) DTPN model after reduction.

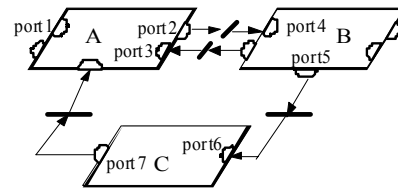


Figure 2 Framework of compositional TPN model

As shows in figure (1c), assume that the global time starts from θ . The initial state is $S_0 = (p1[0,0], p2[0,0], \theta)$. State $S_1 = (p5[0,0], p10[0,0], \theta + 5)$ is reachable through the firing of t_3 . The delay between S_1 and S_0 is $(\theta + 5) - \theta = 5$. The global time is the value. It is unreasonable in TPN model^[1]. We use the interval instead of the value in order to improve the representation. At the same time it lacks the specific schedule analysis method according to concurrency, synchronization and conflict.

Our main contributions include: ① this paper presents several component-level reduction rules based on DTPN, ② an new schedule analysis approach for DTPN which integrates static interval and dynamic interval is proposed.

2. New Schedule Analysis method

There are two changes about the state definition of new method: ① the global time ST is a interval; ② the definition of FT : $(ti[\theta ETi, \theta LTi][R\theta ETi, R\theta LTi]\{TOKi1, \dots, TOKin\}, \dots)$, which associates each transitions with both static firing interval $[R\theta ETi, R\theta LTi]$ and dynamic firing interval $[\theta ETi, \theta LTi]$. Assume the current state is S_k and the global time is ST_k . The procedure of new method is as follow:

- ① If $ti \notin FT_k$, then transition ti is not nonschedulable;
- ② Let $MR\theta LT_k = \min(R\theta LTj / tj \in FT_k)$, $M\theta LT_k = \min(\theta LTj / Tj \in FT_k)$. If $R\theta ETi \leq MR\theta LT_k$, then ti is schedulable at marking M_k . The static firing interval of ti is $[R\theta ETi, MR\theta LT_k]$ and the dynamic firing interval of ti is $[\theta ETi, M\theta LT_k]$;
- ③ $ST_{k+1} = [ST_k, M\theta LT_k]$;
- ④ M' change is the same as above.
- ⑤ Updating EA_k : Removing $TOKmj(pj, tm)$ $[\theta EA_m, \theta LA_m]$ from EA_k , where tm is the output transition of place pj . Adding $TOKsk(pk, ts)$ $[\theta EA_s, \theta LA_s]$ into EA_k , where ts is the output transition of place pk and $[\theta EA_s, \theta LA_s] = ST_{k+1} + [EDA_{ik}, LDA_{ik}] + [EDA_{ks}, LDA_{ks}]$, $[EDA_{ik}, LDA_{ik}]$ is the static interval on $arc(ti, pk)$ and $[EDA_{ks}, LDA_{ks}]$ is the static interval on $arc(pk, ts)$.
- ⑥ Updating FT_k : Removing $tm[\theta ET_m, \theta LT_m]$ $[R\theta ET_m, R\theta LT_m]$ $\{TOKm1, \dots, TOKmn\}$ from FT_k , where $TOKmj \in \{TOKi1, \dots, TOKin\}$. $ts[\theta ET_s, \theta LT_s]$ $[R\theta ET_s, R\theta LT_s]$ $\{TOKs1, \dots, TOKsl, \dots, TOKsn\}$ is added into FT_k , where $TOKsl(pl, ts) \in EA_{k+1}$, $l = 1, \dots, n$. Dynamic firing interval of transition ts is $[\theta ET_s, \theta LT_s] = [ET_s, LT_s] + MAX_{pl}([\theta EA_{ls}, \theta LA_{ls}])$, where $[ET_s, LT_s]$ is the static firing interval of transition ts and $[\theta EA_{ls}, \theta LA_{ls}]$ is the dynamic firing interval on $TOKsl(pl, ts)$ in EA_{k+1} . Relative firing interval of transition ts is $[R\theta EL_s, R\theta LL_s] = [EDA_{hl}, LDA_{hl}] + [EDA_{ls}, LDA_{ls}]$, where $[EDA_{hl}, LDA_{hl}]$ is the static firing interval on $arc(th, pl)$ and $[EDA_{ls}, LDA_{ls}]$ is the static firing interval on $arc(pl, ts)$, th is a input transition of place pl . If $ta[\theta ET_a, \theta LA_a]$ $[R\theta ET_a, R\theta LA_a]$ $\{TOKa1, \dots, TOKan\}$ is an inherited friable transition (enabled by both M_k and M_{k+1}), then modifies its relative firing interval and absolute firing interval $[\theta ET_a', \theta LA_a'] = [\max(\theta ET_a, \theta ET_i), \theta LA_a]$, $[R\theta ET_a', R\theta LA_a'] = [\max(0, R\theta ET_a - RMLT_k), R\theta LA_a - R\theta ET_i]$.

As shows in figure (1c), the initial state $S_0 = (p1[0,0], p2[0,0], [0,0])$. If $t3$ fires, then $S_1 = (p5[0,0], [2,5])$ and $FT_1 = (t4[3,7][1,2], t6[5,9][3,4])$. Analyzing static firing intervals of two friable transitions, it appears that $t4$ happens before $t6$. At the same time, there is a conflict between $t4$ and $t6$, so $t6$ cannot fire. Hence the final state is $S_2 = (p10[3,5], [3,7])$, that is $S_2 = (p10[0,0], [6,12])$. It is obvious that the time delay between S_2 and S_0 is $[6,12]$.

3. Component-level Reduction Rules Based on DTPN

The building blocks of a compositional TPN are component ^[7]. A component is a coarse grained subnet of a TPN. A compositional TPN consists of two basic elements: component TPN and inter-component connections. Figure 2 shows an example of a compositional TPN model. The model has three components – A, B and C. Each component has two parts: ① communication ports (denoted by half circles), including input ports (e.g. port6) and output ports (e.g. port7).

3.1. Reduction Rules

The following definition is useful in the proofs of the following theorems.

Definition 1. Component N_c is transformed into N_c' . N_c satisfies: when all the input ports contain a token, the output port can receive token. If N_c' also satisfies the above condition, we can say N_c' preserves synchronization property. In figure 1 (a), when $p1$ and $p2$ both contain a token, $p5$ can receive a token. The same happen in (b) and (c). So (b) and (c) preserve the synchronization property of (a).

Definition 2. Component N_c is transformed into N_c' . N_c satisfies: when the input port contains a token, all the output ports can receive token. If N_c' also satisfies the above condition, we can say N_c' preserves concurrency property. After the firing of $t3$, $p5$ and $p10$ can receive a token in figure 1 (a). The same

happen in (b) and (c). So (b) and (c) preserve the concurrency property of (a).

Definition 3. Component N_c is transformed into $N_{c'}$. N_c satisfies: when all the input ports contain a token, only one of all the output ports can receive token. If $N_{c'}$ also satisfies the above condition, we can say $N_{c'}$ preserves conflict property. In figure 1 (a), when p_5 contains a token, p_7 receives a token. But in (b), p_7 and p_8 can have a opportunity to obtain a token. In (c), only p_7 can obtains a token. So (c) preserves the conflict property of (a), but (b) don't. If we change the time delay on t_6 into $[1,3]$, (b) also can preserve the conflict property of (a).

Definition 4. There are two component TPN model C and C' . C' is the refined model of C . If two condition are satisfied: ① C and C' have the same input ports and output ports, ② C' is subject to all the constrains of C , then we say that C' is the correct refined model of C .

Component-Level Reduction Rule 1

Let N be the TPN model of a system, and N_c the TPN model of a component in the system. $C.PORT_IN = \{port1\}$, $C.PORT_OUT = \{port2\}$. The component has no enabled transition under the initial marking of N . If

- ① Whenever both $port1$ receives a token, $port2$ is guaranteed to receive a token in the future, and
- ② $port1$ can't receive another token until $port2$ received a token.

Then we can reduce N into N' by replacing N_c with a simple net which is compose of three places: $port1$, $port2$ and one transition: t , such that

- ① $port1^* = port2^* = \{t\}$, ${}^*t = \{port1\}$ ${}^*t = \{port2\}$, which *port1 and $port2^*$ remain unchanged, and transition t also remains the same delay interval, and
- ② $SI(arc(port1, port2)) = SI(t)$, $SI(arc(port1, port2))$ is the delay interval from $port1$ to $port2$. $SI(t)$ is the delay interval on t .

Theorem 1. The component-level reduction rule 1 is timing property preserving. (The proof of theorem 1 can be found in [4])

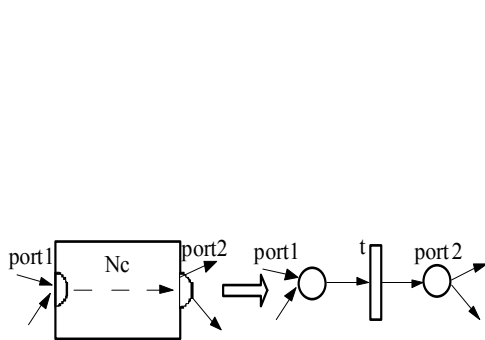


Figure3 Illustration of reduction rule 1

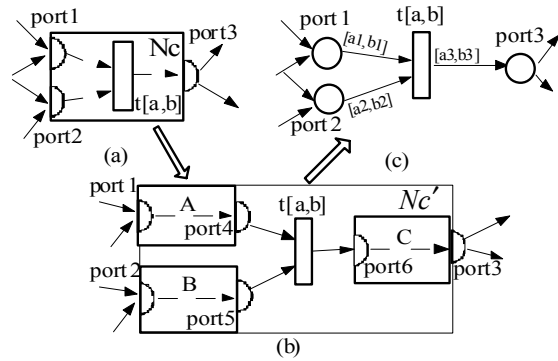


Figure 4 Illustration of reduction rule 2

Component-Level Reduction Rule 2

Let N be the TPN model of a system, and N_c the TPN model of a component in the system. $C.PORT_IN = \{port1, port2\}$, $C.PORT_OUT = \{port3\}$. The component has no enabled transition under the initial marking of N . If

- ① Whenever both $port1$ and $port2$ receive a token, $port3$ is guaranteed to receive a token in the future, and
- ② At least one of $port1$ and $port2$ can't receive another token until $port3$ received a token.

Then we can reduce N into N' by replacing N_c with a simple net which is compose of three places: $port1$, $port2$ and $port3$, and one transition: t , such that

- ① $port1^* = port2^* = port3^* = \{t\}$, ${}^*t = \{port1, port2\}$ ${}^*t = \{port3\}$, which *port1 , *port2 and $port3^*$ remain unchanged, and transition t also remains the same delay interval, and

- ② $SI(arc(port1,t)) = SI_ (port1,*t)$, $SI(arc(port2,t)) = SI(port2.*t)$, $SI(arc(t,port3)) = SI_ (t*,port3)$.
 $SI(arc(port1,t))$ is the delay interval on the arc from $port1$ to t . $SI(arc(t,port3))$ is the delay interval on the arc from t to $port3$.

Theorem 2. The component-level reduction rule 2 is timing property preserving and preserves the synchronization property.

Proof: In figure 4, N_c in (a) is refined into N_c' in (b). Hence we first prove N_c' is the correct refined model of N_c .

$N_c.PORT_IN = \{port1,port2\} = N_c'.PORT_IN$, $N_c.PORT_OUT = \{port3\} = N_c'.PORT_OUT$. It indicates that N_c and N_c' have the same input ports and output ports.

From preconditions ① and ② of N_c , it must contain a synchronizing transition t , which synchronizes tokens sent by $port1$ and $port2$, then new token is received by $port3$.

The principle of constructing N_c' : maintaining the synchronizing transition t and replacing the middle model of $port1 \rightarrow t$, $port2 \rightarrow t$ and $t \rightarrow port3$ by sub-component A, B and C. We can see that N_c' preserves synchronization property of N_c .

Timing constrains are maintained : $SI_ (port1,port4) = SI_ (port1,*t)$, $SI_ (port2,port5) = SI_ (port2,*t)$,
 $SI_ (port6,port3) = SI_ (t*,port3)$.

According to the definition 4, N_c' is the correct refined model of N_c .

Then sub-component A, B and C in (b) are reduced by reduced rule 1, respectively. DTPN model in (c) can be obtained.

So the component-level reduction rule 2 is timing property preserving The proof of theorem 1 can be found in [4].

Component-Level Reduction Rule 3

Let N be the TPN model of a system, and N_c the TPN model of a component in the system. $C.PORT_IN = \{port1\}$, $C.PORT_OUT = \{port2,port3\}$. The component has no enabled transition under the initial marking of N . If

- ① Whenever $port1$ receives a token, $port2$ and $port3$ are guaranteed to receive a token in the future, and
 ② $port1$ can't receive another token until one of $port2$ and $port3$.

Then we can reduce N into N' by replacing N_c with a simple net which is compose of four places: p , $port1$, $port2$ and $port3$, and three transitions: t , $t1$ and $t2$, such that

- ① $port1^* = \{t\}$, $*port2 = \{t1\}$, $*port3 = \{t2\}$, $*p = \{t\}$
 $p^* = \{t1,t2\}$, $t^* = \{p\}$, $*t = \{port1\}$, $t1^* = \{port1\}$,

$*t1 = \{p\}$, $*t2 = \{p\}$, $*t2 = \{port2\}$, which p , $*port1$, $port2^*$ and $port3^*$ remain unchanged, and $t1$ and $t2$ also remain the same delay interval, and

- ② $SI(arc(port1,t) = SI_ (port1,p)$, $SI(arc(t1,port2)) = SI_ (t1*,port2)$, $SI(arc(t2,port3)) = SI_ (t2*,port3)$.

Theorem 3. The component-level reduction rule 3 is timing property preserving, also preserves conflict property.

Proof: In figure 5, N_c in (a) is refined into N_c' in (b). Hence first proves N_c' is the correct refined model of N_c .

$N_c.PORT_IN = \{port1\} = N_c'.PORT_IN$, $N_c.PORT_OUT = \{port2,port3\} = N_c'.PORT_OUT$. It indicates that N_c and N_c' have the same input ports and output ports.

From preconditions ① and ② of N_c model, it must contain a conflict-fork place p that there is a conflict among its output transitions , two forking transitions $t1$ and $t2$ in the inside of component.

The principle of constructing N_c' : maintaining p , $t1$ and $t2$, replacing the middle model of $port1 \rightarrow p$, $t1^* \rightarrow port2$ and $t2^* \rightarrow port3$ by sub-component A, B and C. We can see that N_c' preserves conflict property of N_c .

Timing constrains are maintained: $SI(A) = SI_{port1,p}$, which $SI(A)$ is the delay interval on component A. $SI_{port4,port2} = SI_{t1^*,port2}$, $SI_{port5,port3} = SI_{t2^*,port3}$.

According to the definition 4, Nc' is the correct refined model of Nc .

Then sub-component A, B and C in figure (b) are reduced by rule 1, respectively. DTPN model in figure (c) can be obtained.

So the component-level reduction rule 2 is timing property preserving. The proof of theorem 1 can be found in [4].

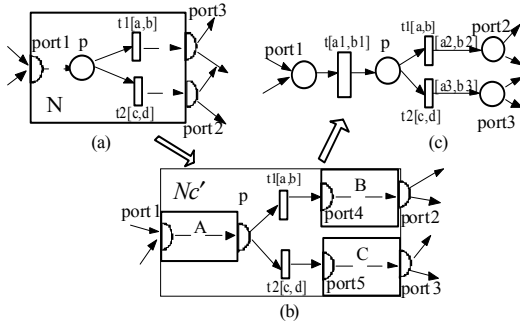


Figure 5 Illustration of reduction rule 3

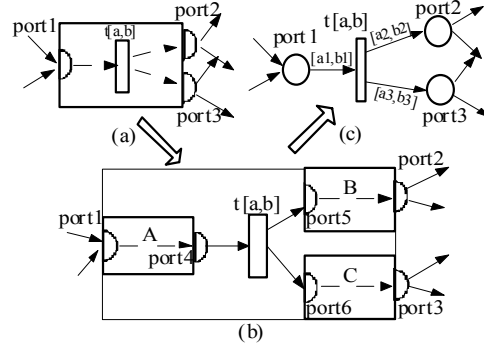


Figure 6 Illustration of reduction rule 4

Component-Level Reduction Rule 4

Let N be the TPN model of a system, and Nc the TPN model of a component in the system. $C.PORT_IN = \{port1\}$, $C.PORT_OUT = \{port2, port3\}$. The component has no enabled transition under the initial marking of N . If

- ① whenever $port1$ receives a token, both $port2$ and $port3$ are guaranteed to receive a token in the future, and
- ② $port1$ can't receive another token until both of $port2$ and $port3$ received a token.

Then we can reduce N into N' by replacing Nc with a simple net which is compose of three places: $port1$, $port2$ and $port3$, and one transition: t , such that

- ① $port1^* = port2^* = port3^* = \{t\}$, ${}^*t = \{port1\}$, $t^* = \{port2, port3\}$, which *port1 , $port2^*$ and $port3^*$ remain unchanged, and transition t also remains the same delay interval, and
- ② $SI(arc(port1,t)) = SI_{port1,t}$, $SI(arc(t,port2)) = SI_{t^*,port2}$, $SI(arc(t,port3)) = SI_{t^*,port3}$.

Theorem 4. The component-level reduction rule 4 is timing property preserving, also preserves concurrency property.

Proof: In figure 6, Nc in (a) is refined into Nc' in (b). Hence first proves Nc' is the correct refined model of Nc .

$$Nc.PORT_IN = \{port1\} = Nc'.PORT_IN,$$

$Nc.PORT_OUT = \{port2, port3\} = Nc'.PORT_OUT$. It indicates that Nc and Nc' have the same input ports and output ports.

From preconditions ① and ② of Nc model, it must contain a concurrency-fork transition t in the inside of component. Through firing finite numbers a token received by $port1$ forked by transition t , then there are two concurrent forks, which are causally independent, one to $port2$ and the other to $port3$.

The principle of constructing Nc' : maintaining the concurrency-fork transition t , replacing the middle model of $port1 \rightarrow t$, $t \rightarrow port2$ and $t \rightarrow port3$ by sub-component A, B and C. We can see that Nc' preserves conflict property of Nc .

Timing constrains are maintained:

$$SI_{port1,port4} = SI_{port1,t}, SI_{port5,port2} = SI_{t^*,port2}, SI_{port6,port3} = SI_{t^*,port3}.$$

According to the definition 1, Nc' is the correct refined model of Nc .

Then sub-component A, B and C in (b) are reduced by rule 1, respectively. DTPN model in (c) can be obtained.

So the component-level reduction rule 2 is timing property preserving . The proof of theorem 1 can be found in [4].

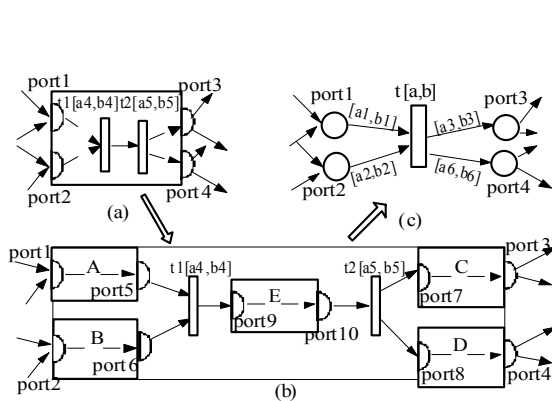


Figure 7 Illustration of reduction rules 5

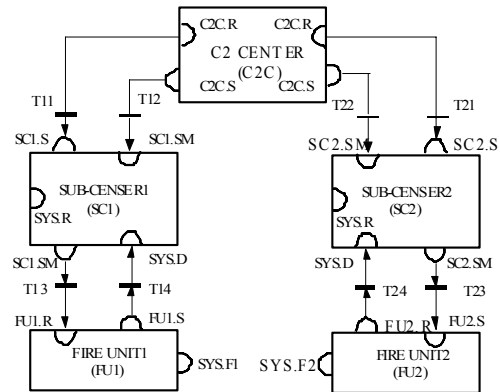


Figure 8 Composition interface of the C2 system

Component-Level Reduction Rule 5

Let N be the TPN model of a system, and N_c the TPN model of a component in the system. $C.PORT_IN = \{port1, port2\}$, $C.PORT_OUT = \{port3, port4\}$. The component has no enabled transition under the initial marking of N . If

- ① Whenever both $port1$ and $port2$ receive a token, both of $port3$ and $port4$ are guaranteed to receive a token in the future, and
- ② At least one of $port1$ and $port2$ cannot receive another token until both of $port3$ and $port4$ received a token.

Then we can reduce N into N' by replacing N_c with a simple net which is composed of three places: $port1$, $port2$, $port3$ and $port4$, and one transition: t , such that

- ① $port1^* = port2^* = port3^* = port4^* = \{t\}$, $^*t = \{port1, port2\}$, $^*t = \{port3, port4\}$, which *port1 , $port2^*$, $port3^*$ and $port4^*$ remain unchanged, and transition t also remains the same delay interval, and
- ② $SI(arc(port1,t)) = SI_ (port1,^*t1)$, $SI(arc(port2,t)) = SI_ (port2,^*t1)$, $SI(t) = SI(t1) + SI(t1^*,t2) + SI(t2)$, $SI(arc(t,port3)) = SI_ (t2^*,port3)$, $SI(arc(t,port4)) = SI_ (t2^*,port4)$.

Theorem 5. The component-level reduction rule 5 is timing property preserving, also preserves synchronization and concurrency property.

Proof: As shows in figure 7, we can see that N_c in reduction rule 5 actually is the compositional model integrating N_c in reduction rule 2 with N_c in reduction rule 4.

Hence, the proof of theorem 5 can refer to the proofs of theorem 2 and theorem 4.

It should noted that:

- ① The component-level reduction rules are developed based on not only the external observable input-output patterns of component bet also the internal properties preserving.
- ② A component may be analyzed by reachability analysis method [8],. This is a fundamental and most widely applied method for analyzing TPNs. If necessary and possible, we can use some individual transition level reduction rules given in [5] to reduce the component before reachability analysis. In case a component is very complicated, we can also use simulation or test to obtain the timing parameters required by its reduced net.

4. Reduction Analysis for C2 system

In this section, we show the application of the improving reduction rules to the verification of timing properties of a command and control (C2) system. Figure 8 is the CTPN model of a anti-air system, which consists of two level command and control centers: one is C2 center (C2C), the other is SUB_CENTER. We focus on the verification of requirements on the time delays in the execution of the system functions.

Suppose that for a specific C2 system with the structure of Figure 8 there are following timing requirements:

(C1) The reaction time of the system must be less or equal to 40 time units.

(C2) The time delay from a detailed firing assignment scheme made by a *SUB_CENTER* to the result of the damage assessment referred to the execution of this scheme received by the same *SUB_CENTER* must be less than or equal to 20 time units.

(C3) Since the bottleneck for information processing is often located in the *C2C*, the center is always asked to respond quickly. This is captured by the requirement that the whole processing time for a group of messages from the two *SUB_CENTER* must be less than or equal to 15 time units.

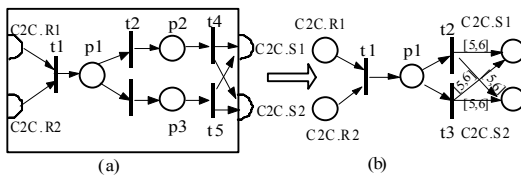


Figure 9 The TPN model of component C2C and reduction model
Firing times: $t_1: [1, 2], t_2: [3, 5], t_3: [3, 5], t_4, t_5: [5, 6]$

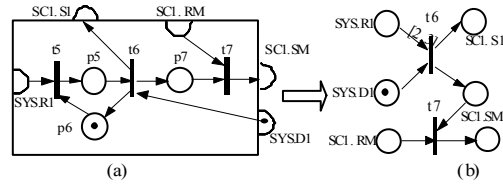


Figure 10 The TPN model of component Sub-Center 1 and reduction model. Firing times: $t_5: [2, 3], t_6: [1, 2], t_7: [4, 6]$

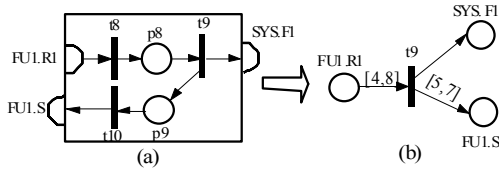


Figure 11 The TPN model of component Fire Unit 1 and reduction model. Firing times: $t_8: [4, 6], t_9: [1, 2], t_{10}: [5, 7]$

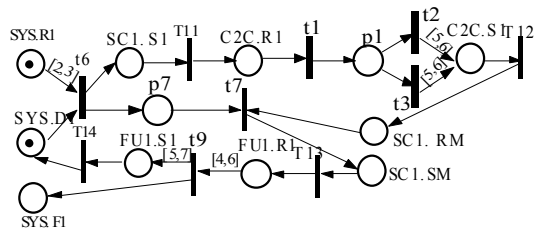


Figure 12 The DTPN model of the system

Figure 9 (a), figure 10 (a) and figure 11 (a) show the internal structure of each TPN component. For detailed representation of each component see [3].

As show in figure 9 (b), figure 10 (b) and figure 11 (b), we apply reduction rules to reduce three components. Through analyzing figure 9(b), we can obtain: $SI(t_2)=[3,5]<[6,7]=SI(t_3)$, t_3 , that is intelligence seat 2, can't work. But t_2 , that is intelligence seat 1, work forever. It is appear that resource is assigned unreasonable. It can be resolved by changing the delay of $t_3: SI(t_3)=[3,5]$. Then DTPN model of the whole system can be obtained in figure 12. Through analyzing, we can compute: $SI(t_{(C2C.R,C2C.S)})=[9,13]<15$, $SI(SC1.SM, SYS.DI)=[12,17]<20$ and $SI(SYS.RI, SYS.FI)=[24,35]<40$. It appears that (C1), (C2) and (C3) are satisfied.

Port/Transaction	Description	C2CR1	C2C received message from Sub_Center1
SYS.RI	A message from Air Radar Group 1 arrived	C2CS1	C2C send command to Sub_Center1
SYS.FI	A combat command to Fire Unit 1 sent	T11	Sub-Center 1 sends information to C2C, Fire time: [1, 1]
SC1.SI	Sub_Center1 ready to send intelligence to C2C	T12	C2C sends command to Sub-Center 1, Fire time: [1, 1]
SC1.RM	Sub_Center 1 received result C2C	T13	Sub_Center 1 sends command to Fire Unit 1, Fire time: [1, 1]
SC1.SM	Sub_Center 1 ready to send command to fire Unit 1	T14	Fire Unit 1 sends result of loss assessment to Sub-Center 1, Fire time [1, 1]
SC1.DI	Sub_Center 1 received result of damage assessment from Fire Unit 1	FU1.R1	Fire Unit 1 received command from Sub-Center 1
FU1.S1	Fire Unit 1 ready to send result of damage assessment to Sub_Center 1		

Table 1 Legends of partial ports in Figure 7

5. Conclusion

This paper improves the existing component-level reduction rules. During the process of transformation new

rules not only preserve timing properties, but also maintain such internal properties of component as conflict and concurrency. At the same time this paper proposes new schedule analysis method based on DTPN model, which offsets the limitation of the existing schedule analysis method [6]. Finally, this paper shows how to apply this reduction rules to timing property verification of the TPN model of a C2 system.

6. Reference

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